PSMN3R9-60XS

N-channel 60 V, 4.0 m Ω standard level MOSFET in TO220F (SOT186A)

12 September 2013

Product data sheet

1. General description

Standard level N-channel MOSFET in TO220F (SOT186A) package qualified to 175 °C. This product is designed and qualified for use in a wide range of industrial, communications and domestic equipment.

2. Features and benefits

- High efficiency due to low switching and conduction losses
- Isolated package
- Suitable for standard level gate drive

3. Applications

- AC-to-DC power supply equipment
- Motor control
- Server power supplies
- Synchronous rectification

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	-	60	V
I _D	drain current	T _{mb} = 25 °C; V _{GS} = 10 V; <u>Fig. 1</u>	[1]	-	-	75	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 2</u>		-	-	55	W
Static charact	eristics						
R _{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C};$ Fig. 12		-	3.25	4	mΩ
Dynamic char	acteristics						
Q_{GD}	gate-drain charge	V _{GS} = 10 V; I _D = 25 A; V _{DS} = 48 V;		-	34.7	-	nC
Q _{G(tot)}	total gate charge	Fig. 13; Fig. 14		-	103	-	nC
Avalanche ruç	ggedness						
E _{DS(AL)S}	non-repetitive drain- source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_D = 75 A; V_{sup} ≤ 60 V; R_{GS} = 50 Ω ; unclamped; Fig. 3		-	-	478	mJ





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[1] Continuous current is limited by package

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	D
2	D	drain		
3	S	source		G UNA
mb		mounting base; isolated		mbb076 S
			TO-220F (SOT186A)	

6. Ordering information

Table 3. Ordering information

Table 6. Grading in	Torritation		
Type number	Package		
	Name	Description	Version
PSMN3R9-60XS	TO-220F	plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-220 "full pack"	SOT186A

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	60	V
V_{DGR}	drain-gate voltage	R_{GS} = 20 k Ω		-	60	V
V_{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{mb} = 100 °C; <u>Fig. 1</u>		-	56	Α
		V _{GS} = 10 V; T _{mb} = 25 °C; <u>Fig. 1</u>	[1]	-	75	Α
I _{DM}	peak drain current	pulsed; $t_p \le 10 \mu s$; $T_{mb} = 25 ^{\circ}C$; Fig. 4		-	318	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 2</u>		-	55	W
T _{stg}	storage temperature			-55	175	°C
T _j	junction temperature			-55	175	°C
T _{sld(M)}	peak soldering temperature			-	260	°C

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Symbol	Parameter	Conditions	Min	Max	Unit
Source-drain	n diode				
Is	source current	T _{mb} = 25 °C	-	46	Α
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$	-	318	Α
Avalanche r	uggedness				
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_D = 75 A; V_{sup} ≤ 60 V; R_{GS} = 50 Ω ; unclamped; Fig. 3	-	478	mJ

[1] Continuous current is limited by package

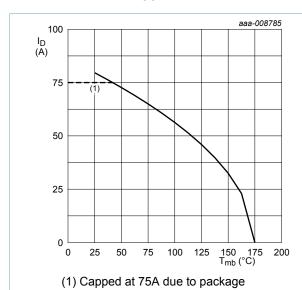


Fig. 1. Continuous drain current as a function of mounting base temperature

$$V_{GS} \ge 10V$$

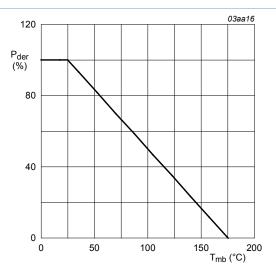


Fig. 2. Normalized total power dissipation as a function of mounting base temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

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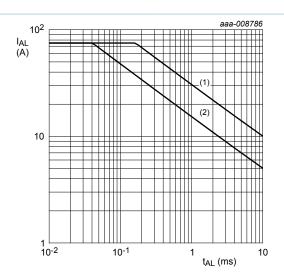


Fig. 3. Single pulse avalanche rating; avalanche current as a function of avalanche time

(1)
$$T_{j (init)} = 25^{\circ}C$$
; (2) $T_{j (init)} = 100^{\circ}C$

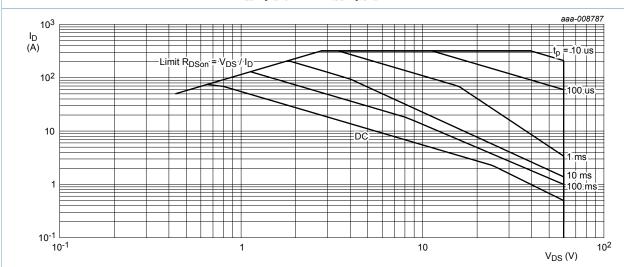


Fig. 4. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

$$T_{mb} = 25^{\circ}C$$
; I_{DM} is a single pulse

8. Thermal characteristics

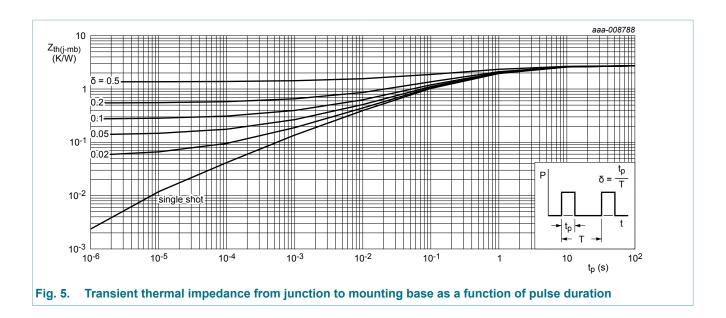
Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	Fig. 5	-	2.5	2.73	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	vertical in free air	-	55	-	K/W

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9. Isolation characteristics

Table 6. Isolation characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
C _{isol}	isolation capacitance		[1]	-	10	-	pF
V _{isol(RMS)}	RMS isolation voltage	50 Hz ≤ f ≤ 60 Hz; RH ≤ 65 %; sinusoidal waveform; clean and dust free		-	-	2500	V

[1] f = 1 MHz

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static charact	teristics					
V _{(BR)DSS}	drain-source	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = -55 °C$	54	-	-	V
	breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _j = 25 °C	60	-	-	V
V _{GS(th)}	gate-source threshold voltage	I_D = 1 mA; V_{DS} = V_{GS} ; T_j = 25 °C; Fig. 9; Fig. 10	2.4	3	4	V
V _{GSth}	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C};$ Fig. 9	-	-	4.5	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C};$ Fig. 9	1	-	-	V
I _{DSS}	drain leakage current	$V_{DS} = 60 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	0.07	1	μA
		V _{DS} = 60 V; V _{GS} = 0 V; T _j = 175 °C	-	-	500	μA

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{GSS}	gate leakage current	$V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	2	100	nA
		V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	2	100	nA
R _{DSon}	drain-source on-state resistance			-	8.7	mΩ
		V_{GS} = 10 V; I_D = 25 A; T_j = 25 °C; Fig. 12	-	3.25	4	mΩ
R_G	gate resistance	f = 1 MHz	-	0.71	-	Ω
Dynamic cl	haracteristics				1	
Q _{G(tot)}	total gate charge	I _D = 25 A; V _{DS} = 48 V; V _{GS} = 10 V;	-	103	-	nC
Q_{GS}	gate-source charge	Fig. 13; Fig. 14	-	25.1	-	nC
Q_{GD}	gate-drain charge		-	34.7	-	nC
C _{iss}	input capacitance	V _{DS} = 25 V; V _{GS} = 0 V; f = 1 MHz;	-	5494	-	pF
C _{oss}	output capacitance	T _j = 25 °C; <u>Fig. 15</u>	-	743	-	pF
C _{rss}	reverse transfer capacitance		-	455	-	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 30 \text{ V}; R_L = 1 \Omega; V_{GS} = 10 \text{ V};$	-	30.6	-	ns
t _r	rise time	$R_{G(ext)} = 5 \Omega$	-	71.2	-	ns
t _{d(off)}	turn-off delay time		-	63.7	-	ns
t _f	fall time		-	64.4	-	ns
Source-dra	in diode		l l	1		
V _{SD}	source-drain voltage	I _S = 10 A; V _{GS} = 0 V; T _j = 25 °C; <u>Fig. 16</u>	-	0.76	1.2	V
t _{rr}	reverse recovery time	$I_S = 25 \text{ A}; dI_S/dt = -100 \text{ A/}\mu\text{s}; V_{GS} = 0 \text{ V};$	-	40.5	-	ns
Q _r	recovered charge	V _{DS} = 30 V	-	53	-	nC

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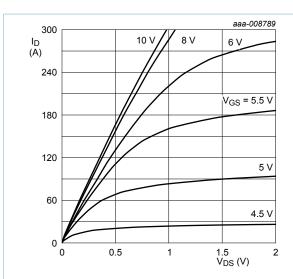


Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

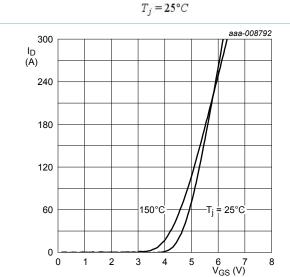


Fig. 8. Transfer characteristics; drain current as a function of gate-source voltage; typical values

$$V_{DS} = 10V$$

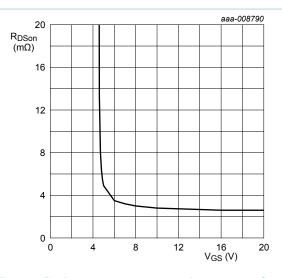


Fig. 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

$$T_j = 25^{\circ}C; I_D = 25A$$

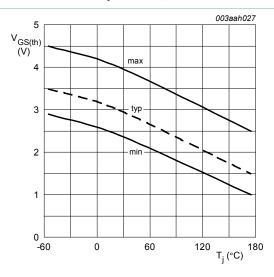


Fig. 9. Gate-source threshold voltage as a function of junction temperature

$$I_D = 1 \text{ mA}; \ V_{DS} = V_{GS}$$

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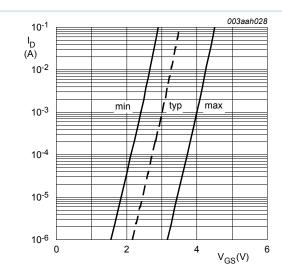


Fig. 10. Sub-threshold drain current as a function of gate-source voltage

$$T_j = 25^{\circ}C; \ V_{DS} = 5V$$

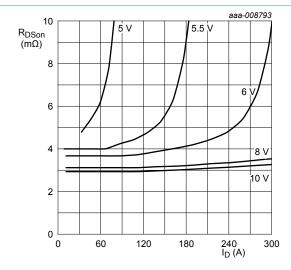


Fig. 12. Drain-source on-state resistance as a function of drain current; typical values

$$T_j = 25^{\circ}C$$

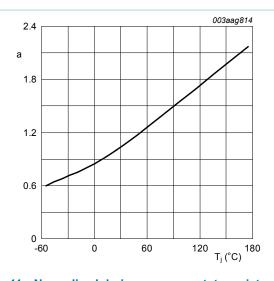


Fig. 11. Normalized drain-source on-state resistance factor as a function of junction temperature

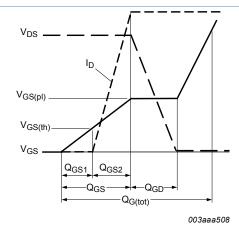


Fig. 13. Gate charge waveform definitions

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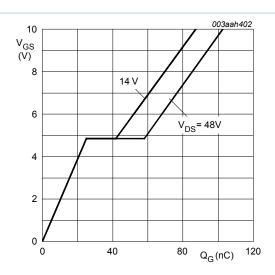


Fig. 14. Gate-source voltage as a function of gate charge; typical values

$$T_j = 25^{\circ}C; I_D = 25A$$

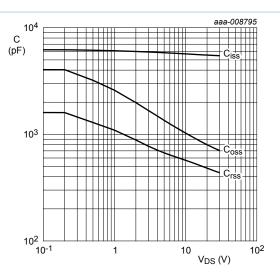


Fig. 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

$$V_{GS} = \mathbf{0}V; f = \mathbf{1}MHz$$

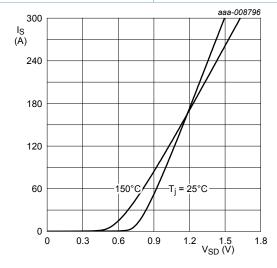
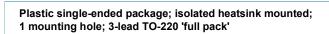


Fig. 16. Source current as a function of source-drain voltage; typical values

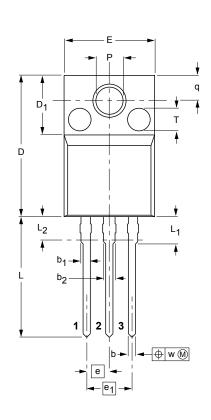
$$V_{GS} = 0V$$

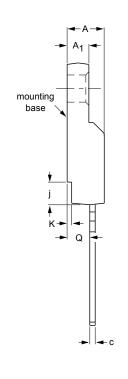
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11. Package outline



SOT186A





0 5 10 mm

DIMENSIONS (mm are the original dimensions)

UNIT	Α	A ₁	b	b ₁	b ₂	С	D	D ₁	E	е	e ₁	j	К	L	L ₁	L ₂ ⁽¹⁾ max.	Р	Q	q	T ⁽²⁾	w
mm	4.6 4.0	2.9 2.5	0.9 0.7	1.1 0.9	1.4 1.0	0.7 0.4	15.8 15.2	6.5 6.3	10.3 9.7	2.54	5.08	2.7 1.7	0.6 0.4	14.4 13.5	3.30 2.79	3	3.2 3.0	2.6 2.3	3.0 2.6	2.5	0.4

Notes

- 1. Terminal dimensions within this zone are uncontrolled.
- 2. Both recesses are # 2.5×0.8 max. depth

OUTLINE		EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE
SOT186A		3-lead TO-220F			-02-04-09 06-02-14

Fig. 17. Package outline TO-220F (SOT186A)

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Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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